

Claims:

- 1 1. A segmented digital to analog converter, comprising:
2 an input for receiving an input signal;
3 a first segment configured to receive and convert one set of digital bits of the
4 input signal to an analog signal;
5 a second segment configured to receive and convert a second set of digital bits of
6 the input signal to an analog signal, the second segment having series of resistors
7 configured to receive the second set of digital bits, a first current source connected at one
8 end of the series of resistors, and a second current source connected at another end of the
9 series of resistors; and
10 an output for outputting an analog signal.

- 1 2. A segmented digital to analog converter according to Claim 1, wherein
2 current is transmitted between the first and second current source in a manner that
3 substantially removes error in the transmission of the second set of digital bits.

- 1 3. A segmented digital to analog converter according to Claim 1, wherein
2 current is transmitted between the first and second current source in a manner that
3 substantially removes error in the transmission of the second set of digital bits without
4 creating a disturbance in the circuit as a whole.